

U.S. PTO
10/034821
12/27/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10034821	FILING DATE 12/27/2001	CLASS 345	SUBCLASS 99	GAU 2674 2874	EXAMINER HJERPE LIM
----------------------	---------------------------	--------------	----------------	---------------------	------------------------

**APPLICANTS: Kim Ha Sook; Choi Gyo Un;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2000-83246 12/27/2000

BEST AVAILABLE COPY

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO CU-2789 VE
Verified and Acknowledged Examiner's initials		
TITLE : Liquid crystal display having gate driving signal line in panel and correction circuit		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING	
TERMINAL DISCLAIMER			Sheets Drwg.	Figs. Drwg.
PREPARED FOR ISSUE		Application Examiner		
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

DISK (CRF)

CD-ROM
(Attached in pocket on right inside flap)